

DOCKET NO.: H0498.70154US00

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Lieber, et al.

Serial No.:

09/935,776

Confirmation No.:

8935

Filed:

August 22, 2001

For:

DOPED ELONGATED SEMICONDUCTORS, GROWING SUCH SEMICONDUCTORS, DEVICES INCLUDING

SUCH SEMICONDUCTORS AND FABRICATING SUCH

**DEVICES** 

Examiner:

Shouxiang Hu

Art Unit:

2811

#### **CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to MAIL STOP AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the \_\_\_\_\_\_ day of June, 2005.

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MAIL STOP AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

> STATEMENT FILED PURSUANT TO THE DUTY OF DISCLOSURE UNDER 37 C.F.R. §§1.56, 1.97 AND 1.98

Sir:

Pursuant to the duty of disclosure under 37 C.F.R. §§1.56, 1.97 and 1.98, the Applicants request consideration of this Information Disclosure Statement.

# PART I: Compliance with 37 C.F.R. §1.97

This Information Disclosure Statement has been filed more than three months after the filing date of this application and after the mailing date of the first Office Action, but before the mailing date of either a final action under 37 C.F.R. §1.113 or a Notice of Allowance under 37 C.F.R. §1.311, or an action that otherwise closes prosecution in this application. The fee of \$180.00 as set forth in 37 C.F.R. §1.17(p) is enclosed.

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#### PART II: Information Cited

The Applicants hereby make of record in the above-identified application the information listed on the attached form PTO-1449 (modified). The order of presentation of the references should not be construed as an indication of the importance of the references.

The Applicants hereby make the following additional information of record in the above-identified application.

The Applicants would like to bring to the Examiner's attention the following co-pending applications that may contain subject matter related to this application:

Serial No.	Filing Date	<u>Inventor(s)</u>
10/812,653	03/29/04	Lieber, et al.
10/973,665	10/26/04	Lieber, et al.
10/995,075	11/22/04	Whang, et al.
11/058,443	02/14/05	Lieber, et al.

### PART III: Remarks

Documents cited anywhere in the Information Disclosure Statement are enclosed unless otherwise indicated. It is respectfully requested that:

- 1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;
- 2. The enclosed form PTO-1449 be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application;
- 3. The citations for the information be printed on any patent which issues from this application.

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By submitting this Information Disclosure Statement, the Applicants make no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

By submitting this Information Disclosure Statement, the Applicants make no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

By submitting this Information Disclosure Statement, the Applicants make no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined by 35 U.S.C. §102.

Notwithstanding any statements by the Applicants, the Examiner is urged to form his own conclusion regarding the relevance of the cited information.

An early and favorable action is hereby requested.

Respectfully submitted,

By:

Timothy J. Over, Ph.D., Reg. No. 36,628

Tani Chen, Sc.D., Reg No. 52,728 Wolf, Greenfield & Sacks, P.C.

600 Atlantic Avenue

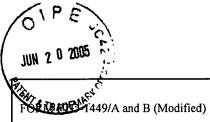
Boston, Massachusetts 02210-2206

Telephone: (617) 646-8000

Docket No.: H0498.70154US00

Date: June , 2005

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Sheet

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

APPLICATION NO.: 09/935,776

ATTY. DOCKET NO.: H0498.70154US00

FILING DATE:

August 22, 2001

CONFIRMATION NO.: 8935

APPLICANT:

Lieber, et al.

GROUP ART UNIT: 2811 of 2 1

EXAMINER:

Shouxiang Hu

#### U.S. PATENT DOCUMENTS

Examiner's Initials	Cite	U.S. Patent Document		Name of Patentee or Applicant of Cited	Date of Publication or of issue
	No.	Number	Kind Code	Document	of Cited Document DD-MM-YYYY
		5,539,214		Lynch, et al.	07/23/1996
		5,776,748		Singhvi, et al.	07/07/1998
		5,900,160		Whitesides, et al.	05/04/1999
		6,060,121		Hidber, et al.	05/05/2000
		6,180,239		Whitesides, et al.	01/30/2001
		6,314,019		Kuekes, et al.	11/06/2001
		6,355,198		Kim, et al.	03/12/2002
		6,459,095		Heath, et al.	10/01/2002
· ·		6,781,166		Lieber, et al.	08/24/2004
		6,882,051		Majumdar, et al.	04/19/2005
-		2003/0089899		Lieber, et al.	05/15/2003
		2003/0124509		Kenis, et al.	07/03/2003
		2003/0186544	A1	Yoshitaka, et al.	10/02/2003
		2004/0113138		DeHon, et al.	06/17/2004
		2004/0113139		DeHon, et al.	06/17/2004
		2004/0213307		Lieber, et al.	10/28/2004
		2005/0109989		Whiteford, et al.	05/26/05

OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.			
		Office Action mailed 01/03/2005 in U.S. Patent Application No. 10/196,337 filed July 16, 2002.			
		Office Action mailed 03/14/2005 in U.S. Patent Application No. 10/020,004, filed 12/11/2001.			
		International Search Report mailed 15 October 2002 in PCT Application No. PCT/US01/48230, Int'l Filing Date, 12/11/2001			
		Written Opinion mailed 10 March 2003 in PCT Application No. PCT/US01/48230, Int'l Filing Date, 12/11/2001			
		DUAN, X., et al., "High-performance thin-film transistors using semiconductor nanowires and nanoribbons", 2003, Nature, Vol. 425, pages 274-278.			
		GUO, Lingjie, et al., "Nanoscale Silicon Field Effect Transistors Fabricated Using Imprint Lithography," Appl. Phys. Lett. 71 (13), 29 September 1997, pages 1881 - 1883			
		GUO, LINGJIE, et al., "A Silicon Single-Electron Transistor Memory Operating at Room Temperature," Science, 31 January 1997, Vol. 275, pages 649-651			
	1	HUANG, et al., "Logic Gates and Computation from Assembled Nanowire Building Blocks", 2001, Science, Vol. 294, pp. 1313-1317.			
		LAUHON, et al., "Semiconductor Nanowire Heterostructures," Phil. Trans. R. Soc. Lond. A, 2004, Vol. 362, pages 1247-1260.			
<u></u>		MCALPINE, M., "Nanoimprint Lithography for Hybrid Plastic Electronics", Nano-Letters, 2003, Vol. 3, No. 4, pages 443-445.			
		MCALPINE, M. "High-Performance Nanowire Electronics and Photonics on Glass and Plastic Substrates", Nano Letters, 2003			

EODM DTO 1	1440/A and D	(Madific	d)	APPLICATION NO.:	09/935,776	ATTY. DOCKET NO.: H0498.70154US00
FORM PTO-1449/A and B (Modified)			-	FILING DATE:	August 22, 2001	CONFIRMATION NO.: 8935
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		APPLICANT:	Lieber, et al.	-		
			· · ·	GROUP ART UNIT:	2811	EXAMINER: Shouxiang Hu
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	PADESTE, et al., "Modular Amperometric Immunosensor Devices", 1995, 8 <sup>th</sup> International Conference on Solid-State Sensors an Actuators and Eurosensors, June 25-29, 1995, pages 487-490.  TAKAYAMA, S., et al., "Patterning cells and their environments using multiple laminar fluid flows in capillary networks", Proc. Natl. Acad. Sci., 1999, Vo. 96, pp. 5545-5548.  TIEFENAUER, et al., "Towards Amperometric Immunosensor Devices", 1997, Biosensors and				
EXAMINER:	Bioelectronics, Vol. 12, No. 3, pages 213-223.  DATE CONSIDERED:				

#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

\*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. \_\_\_\_ filed \_\_\_\_, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

[NOTE - The Office hereby waives the requirement under 37 CFR 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication for

[NOTE - The Office hereby waives the requirement under 37 CFR 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC 371 after June 30, 2003. See 37 CFR 1.491(b). For all patent applications filed on or before June 30, 2003, copies of cited U.S. patents and patent application publications are still required unless an eIDS is filed. Copies of all other patent(s), publication(s), or other information listed must still be provided, even if it was previously submitted to, or cited by, the U.S. Patent Office in an earlier application, unless the earlier application is identified by the IDS and is relied upon for an earlier filing date under 35 U.S.C. §120, and the copy was provided in the earlier application.]